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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor : Chiyoshi Sasaki, et al.  
Serial No : 10/037,481  
Filed : October 24, 2001  
Title : Circuit Substrate Unit and Electronic...  
Group Art Unit : 2836

May 21, 2002

Attn: Customer Corrections Division  
Assistant Commissioner for Patents  
Washington, D.C. 20231


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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/037,481	10/24/2001	2836	924	100809- 00050(SCET 19.098)	15	23	2

026304  
ROSENMAN & COLIN LLP  
575 MADISON AVENUE  
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CONFIRMATION NO. 5216  
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\*OC000000007770846\*

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## Applicant(s)

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Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2000-325992 41/25/2000  
10/25/2000

If Required, Foreign Filing License Granted 01/31/2002

Projected Publication Date: 07/11/2002

Non-Publication Request: No

Early Publication Request: No

## Title

.Circuit substrate unit and electronic equipment

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TC 2800 MAIL ROOM

Preliminary Class  
307

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